

Fig. 1

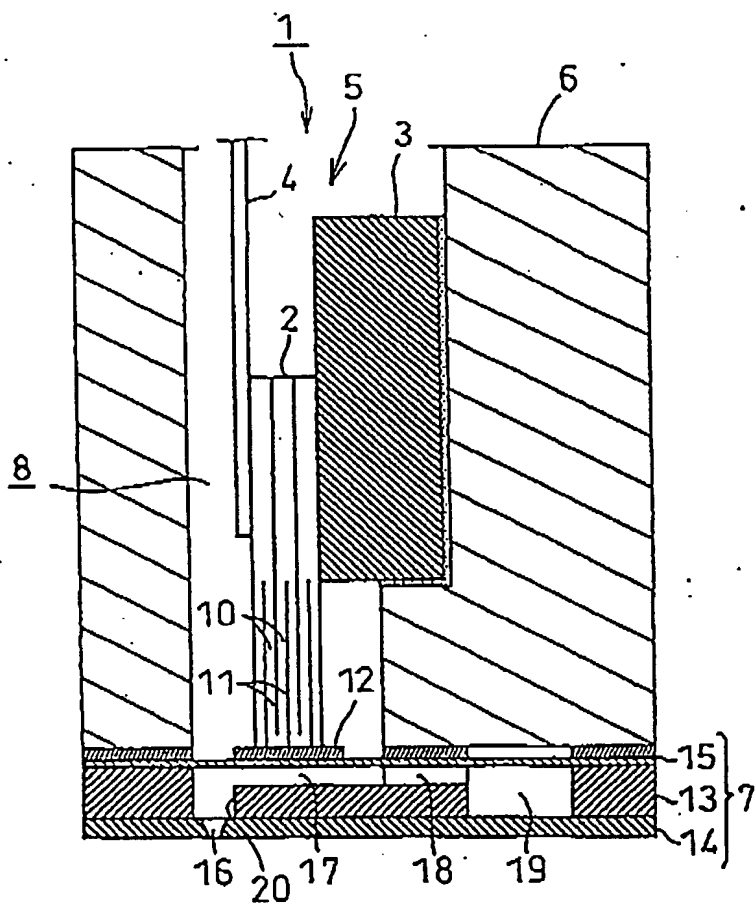


Fig. 2

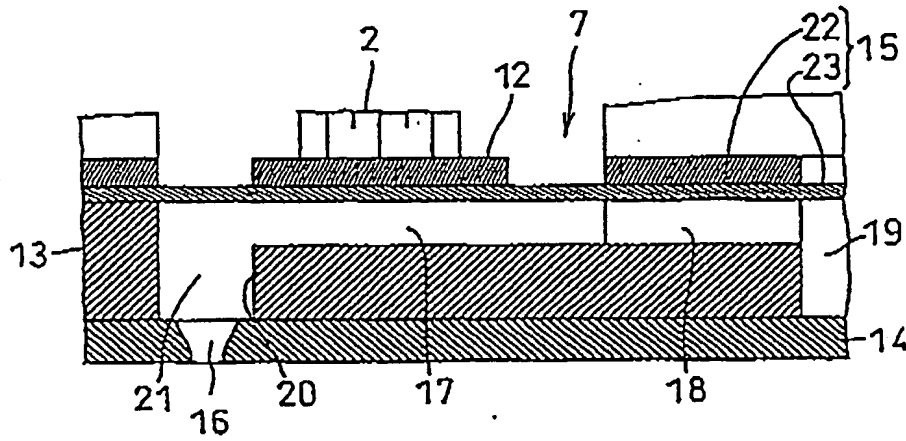
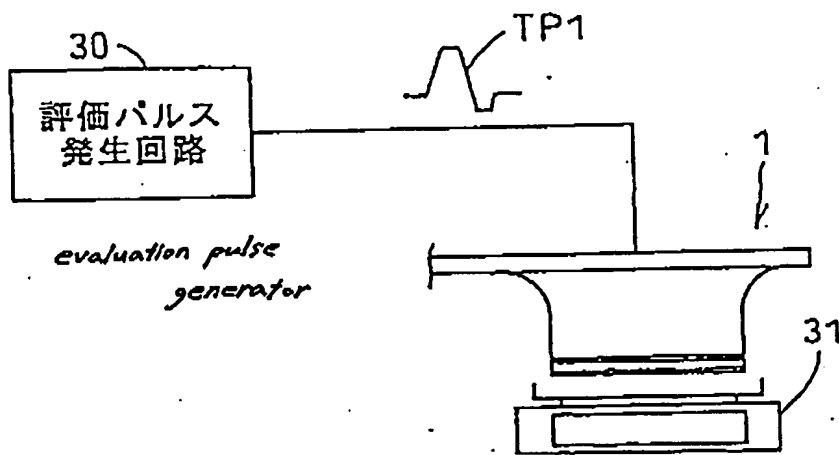


Fig. 3



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Fig. 4

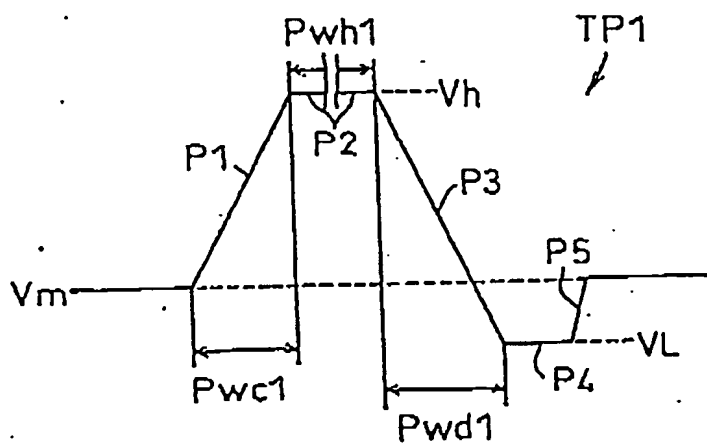


Fig. 5

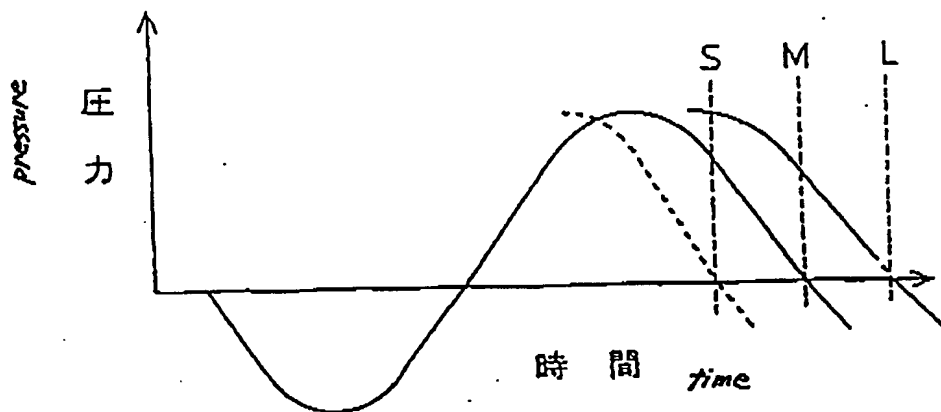
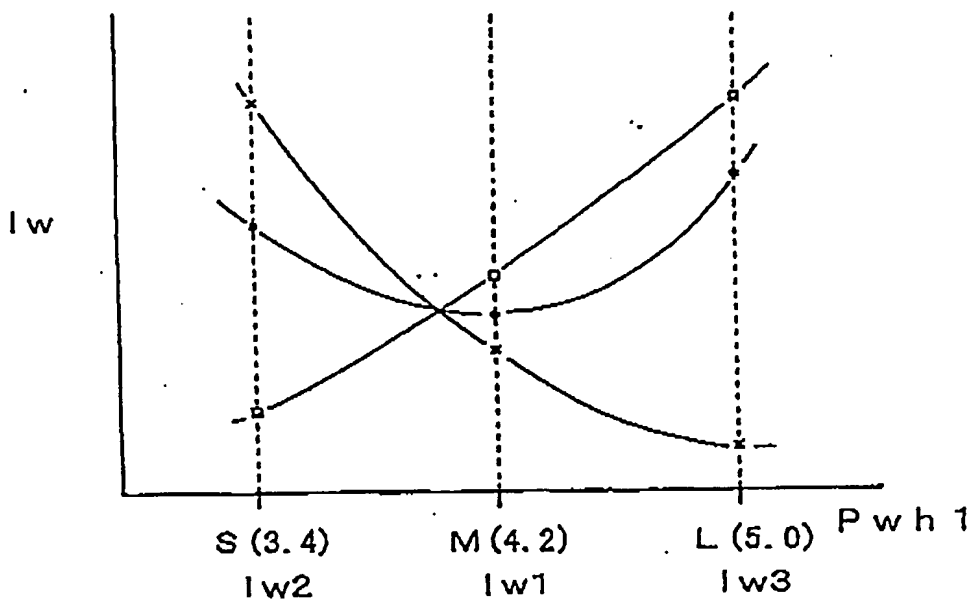


Fig. 6

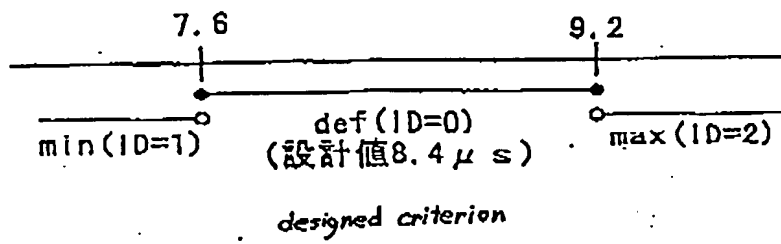


		lw			
Tc ランク D エラー	0	$lw2 > lw1 < lw3$			↘
		$lw2 > lw1 = lw3$			↘↘
		$lw2 = lw1 < lw3$			↘↘↘
	1	$lw2 < lw1 < lw3$			↗
	2	$lw2 > lw1 > lw3$			↗↗
	エラー	$lw2 = lw1 = lw3$			↗↗↗
		$lw2 = lw1 > lw3$			↗↗↗↗
		$lw2 < lw1 = lw3$			↗↗↗↗
		$lw2 < lw1 > lw3$			↗↗↗↗

Fig. 7

Tc rank ID error

Fig. 8



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Fig. 9

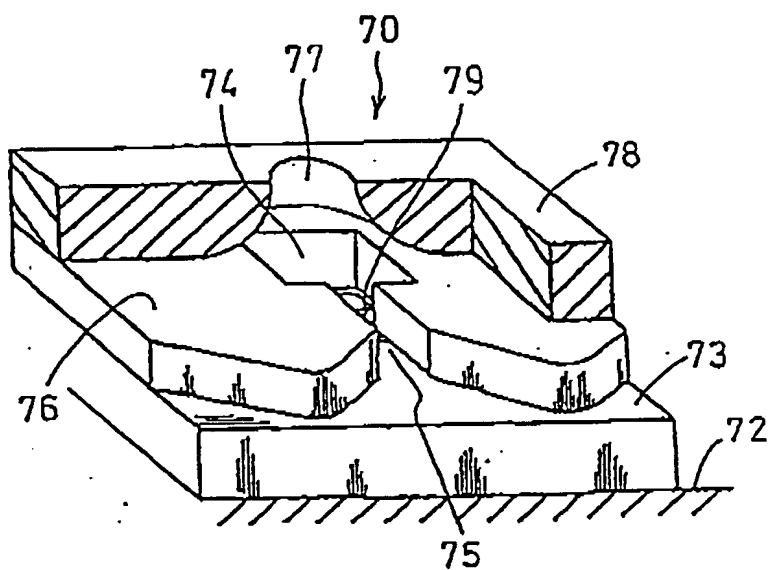
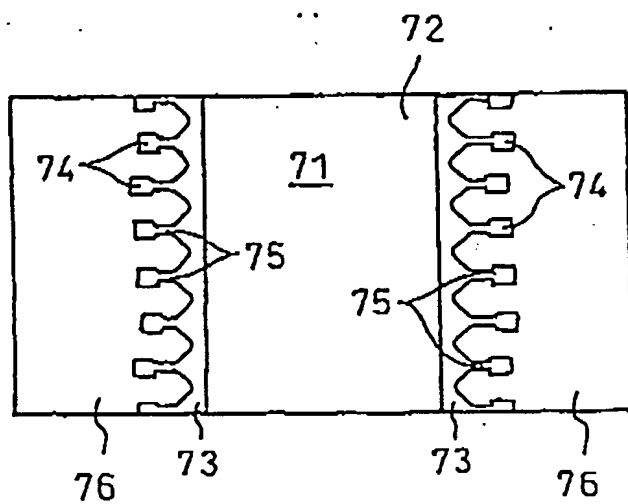


Fig. 10



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Fig. 11

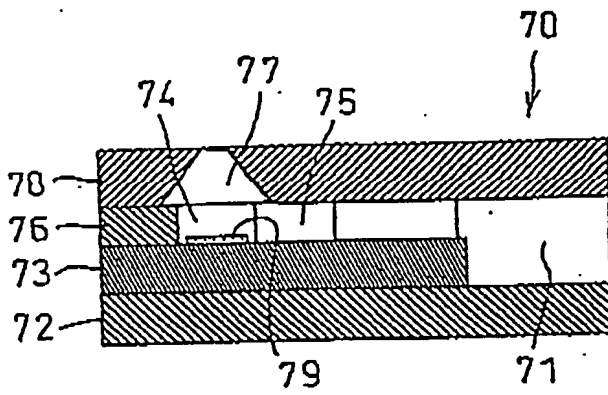


Fig. 12A

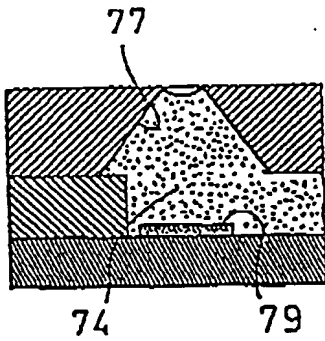
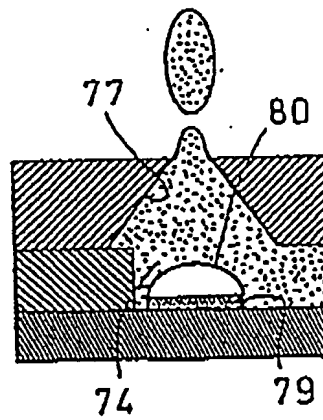
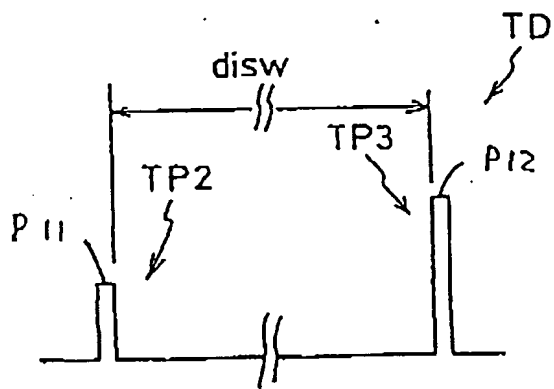


Fig. 12B



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Fig. 13



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Fig. 14

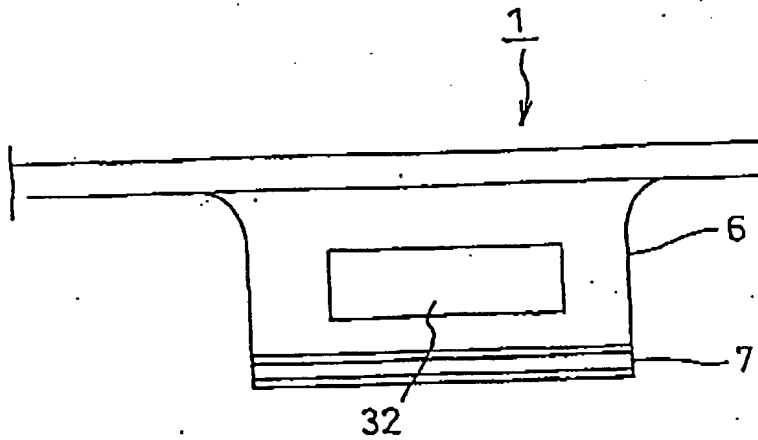
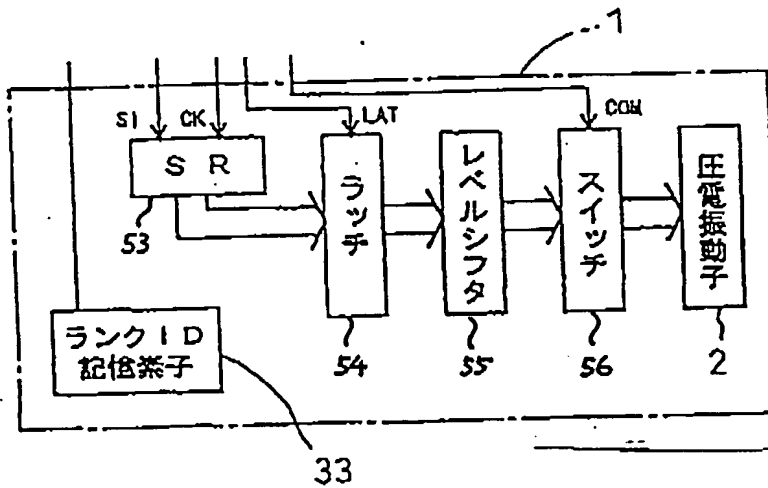
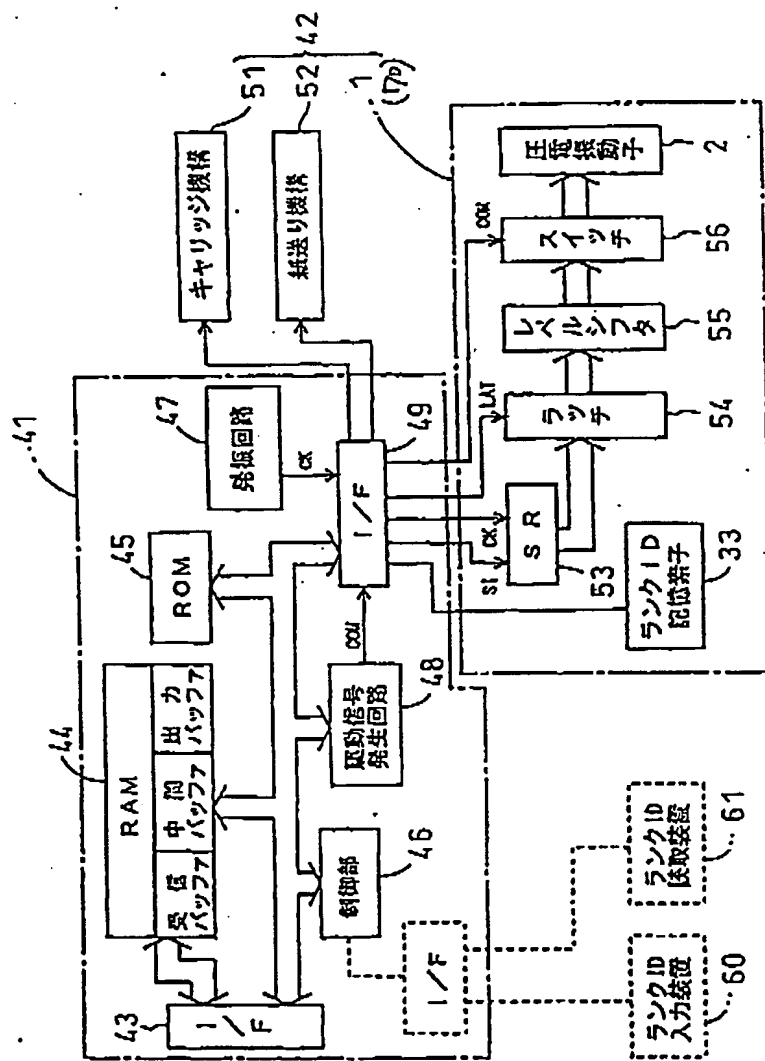


Fig. 15



- 2: piezoelectric vibrator
- 33: rank memory
- 53: shift register
- 54: latch
- 55: level shifter
- 56: switcher

Fig. 16



- 2: piezoelectric vibrator
- 33: rank ID memory
- 44: reception buffer
- 45: intermediate buffer
- 46: output buffer
- 47: controller
- 48: oscillator
- 51: drive signal generator
- 52: carriage feeding mechanism
- 53: paper feeding mechanism
- 54: shift register
- 55: latch
- 56: level shifter
- 60: rank ID input device
- 61: rank ID reader

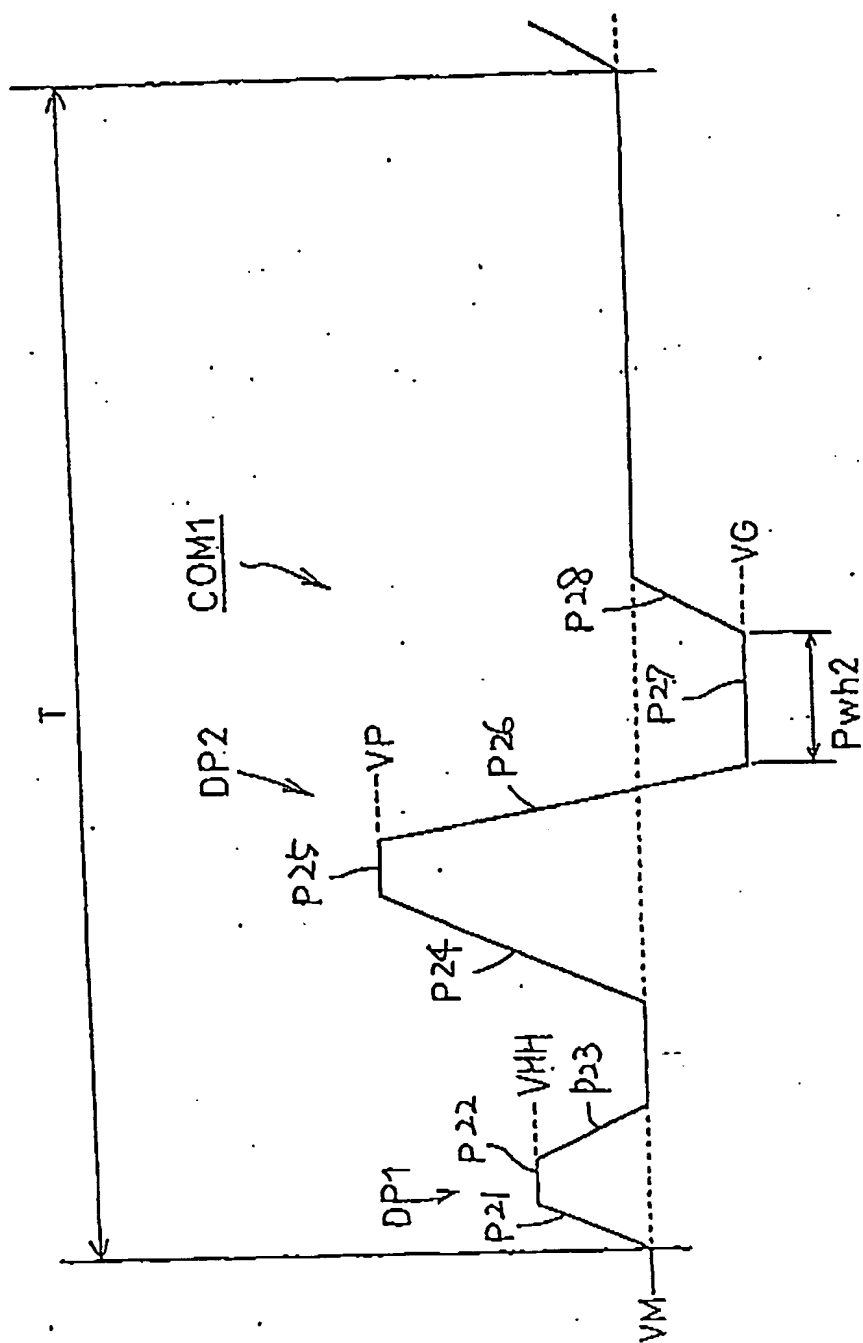
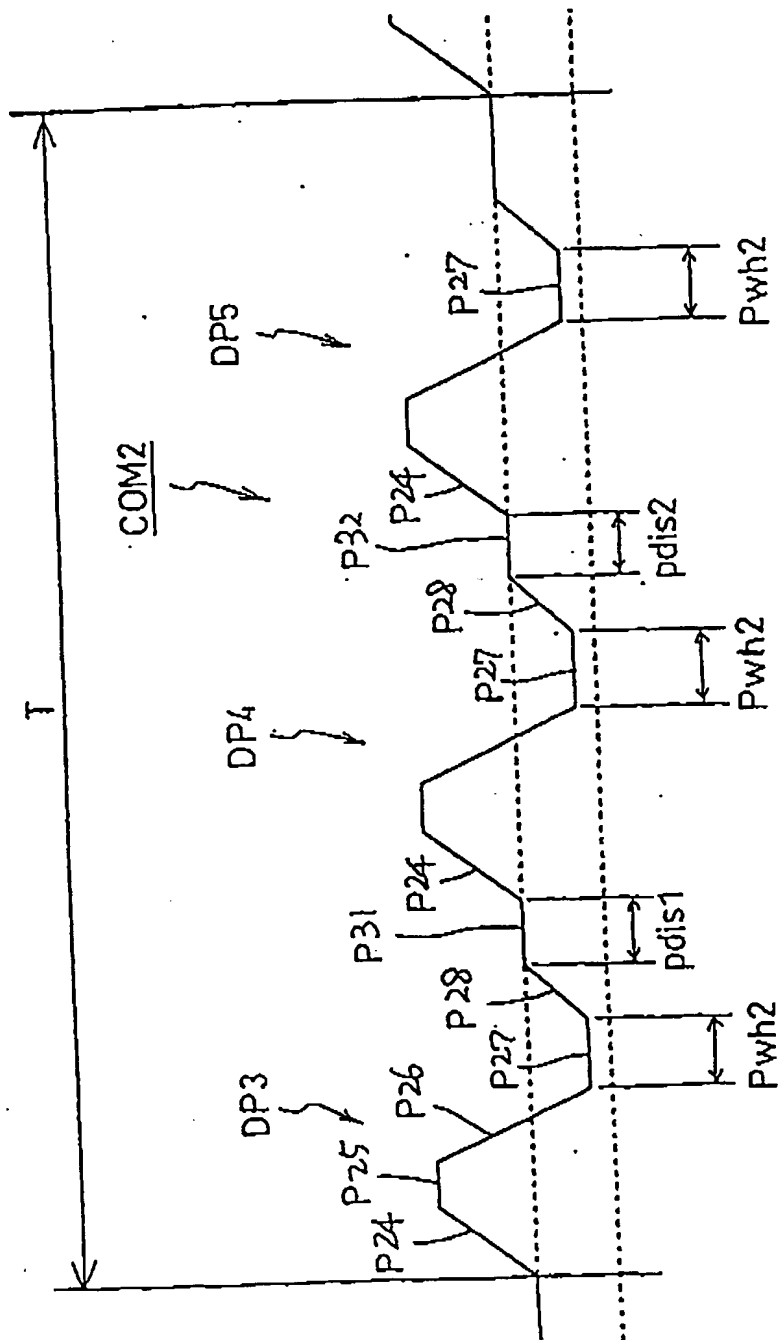


Fig. 18



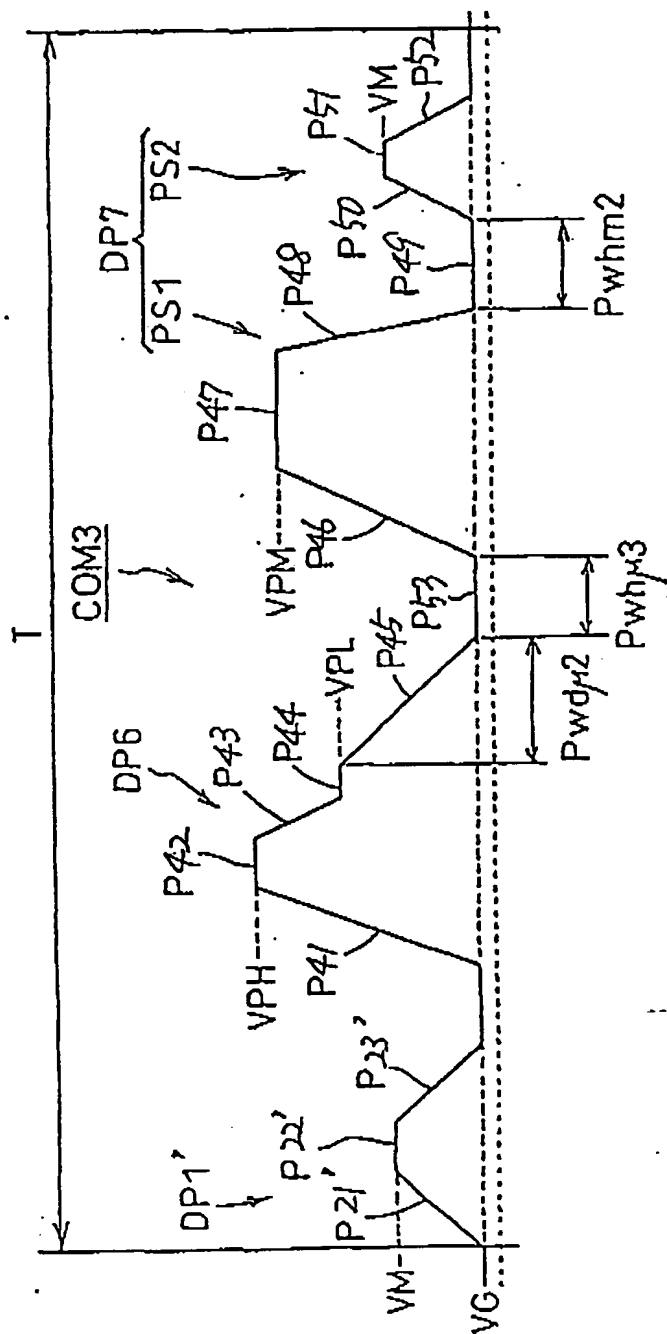


Fig. 20

COM4

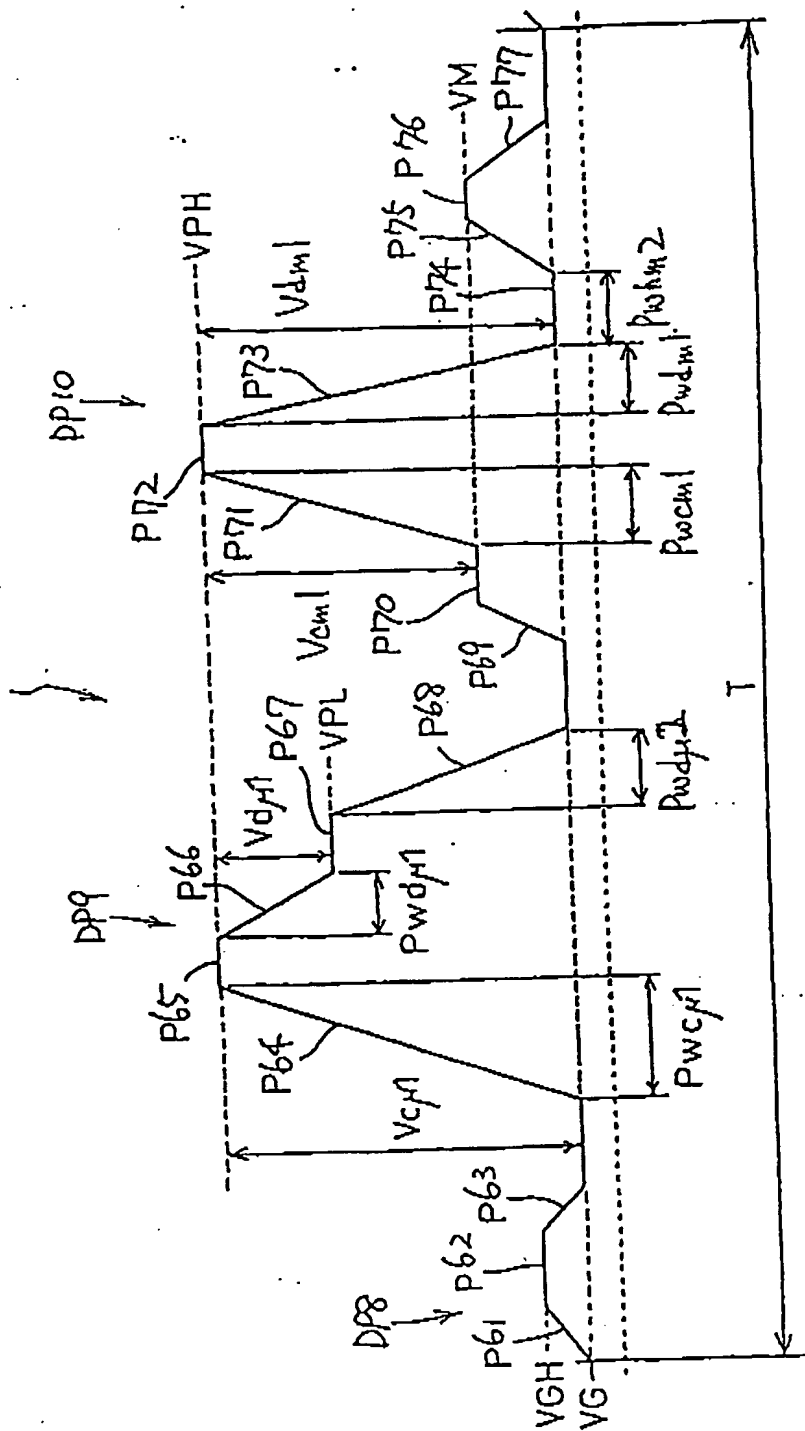
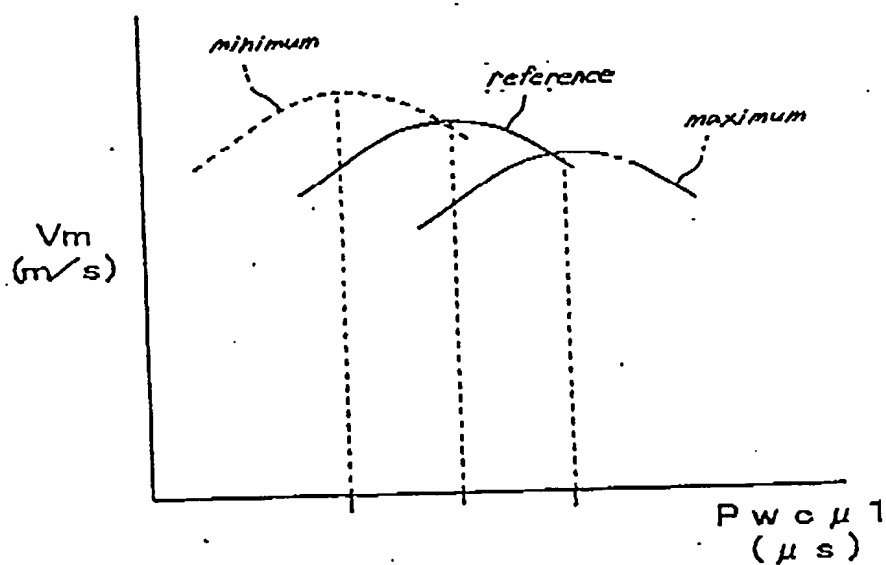


Fig. 21



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Fig. 22

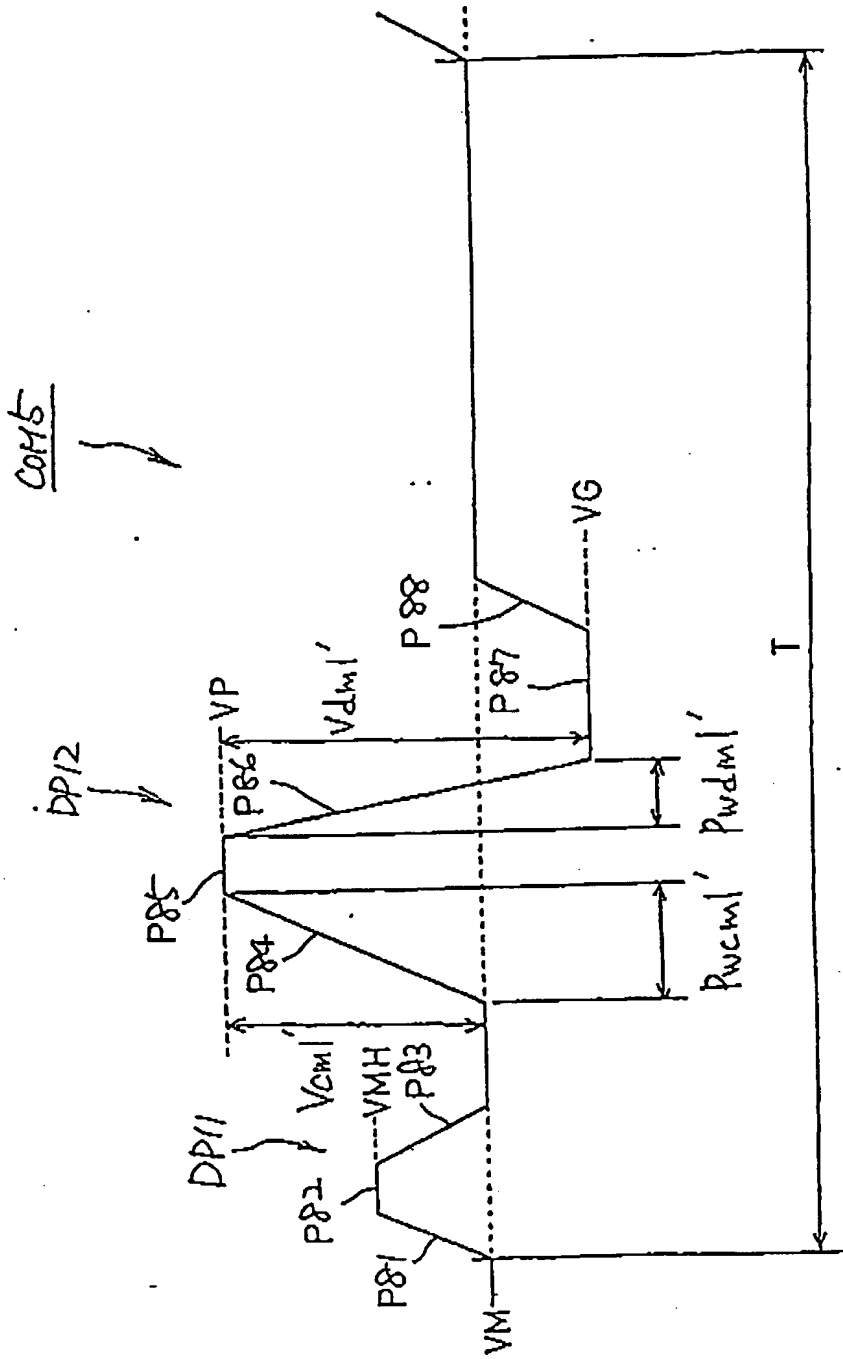


Fig. 23

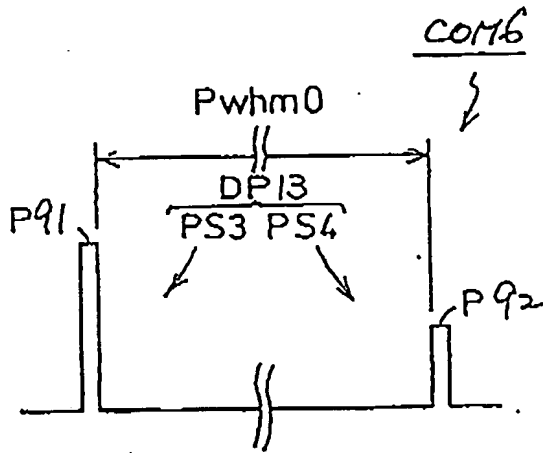
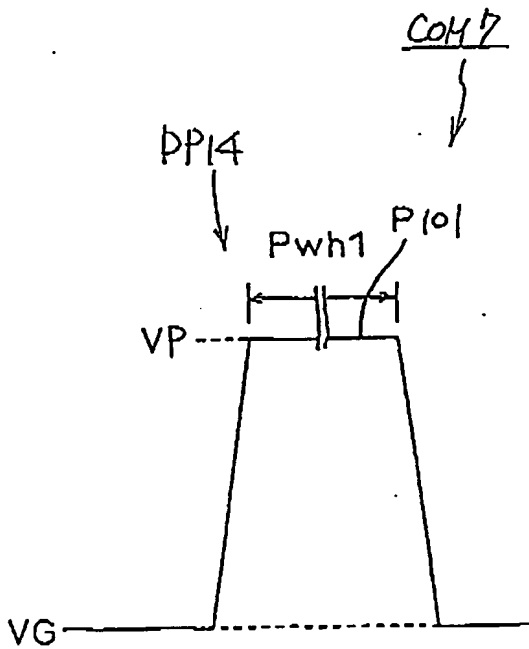


Fig. 24



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